

Fig. 1

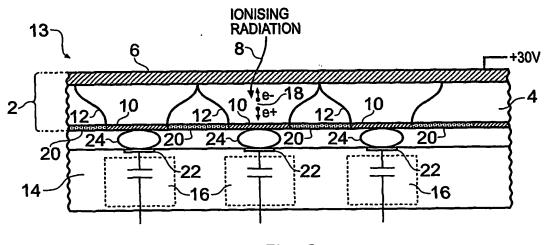


Fig. 2

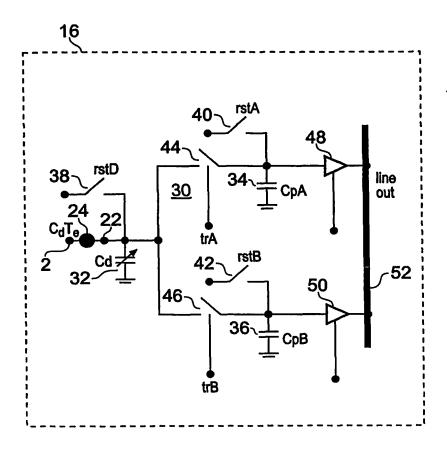


Fig. 3

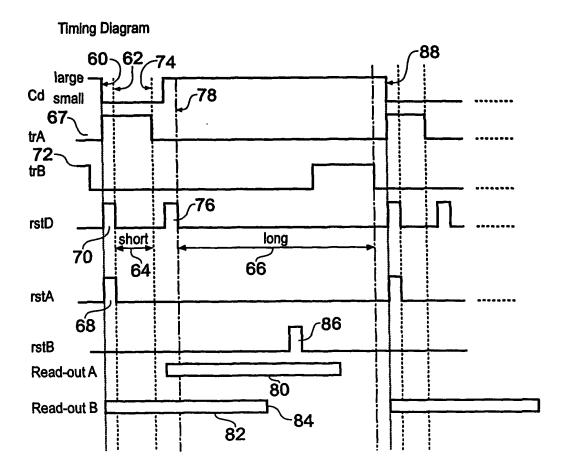


Fig. 4

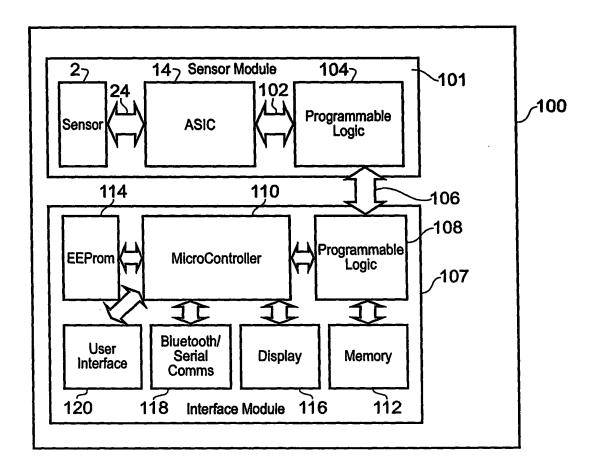


Fig. 5(a)

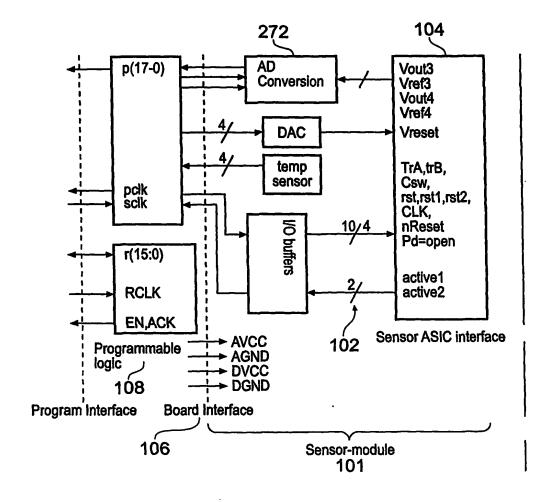


Fig. 5(b)

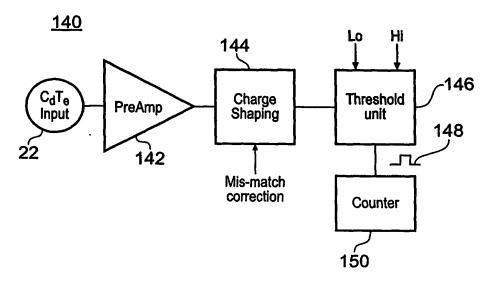


Fig. 6

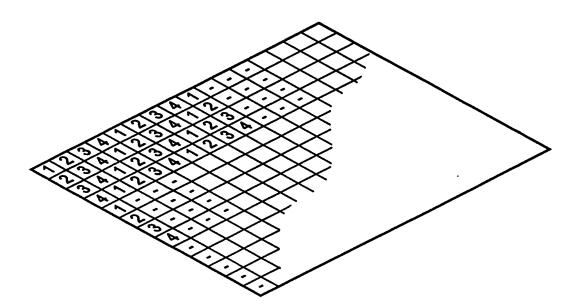


Fig. 7(a)

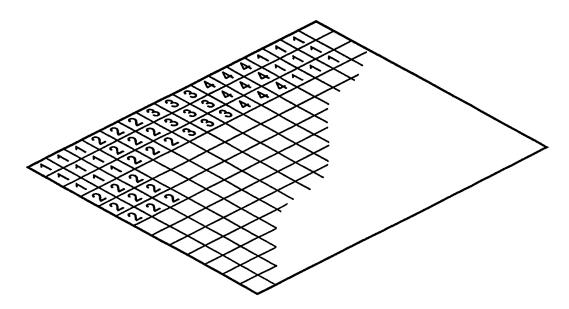


Fig. 7(b)

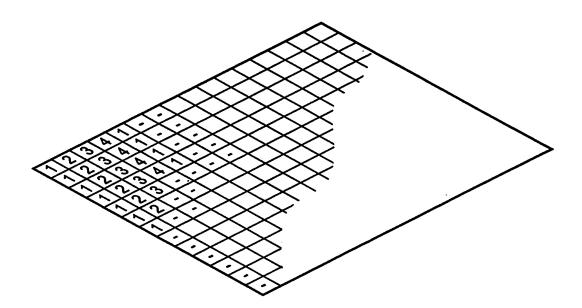
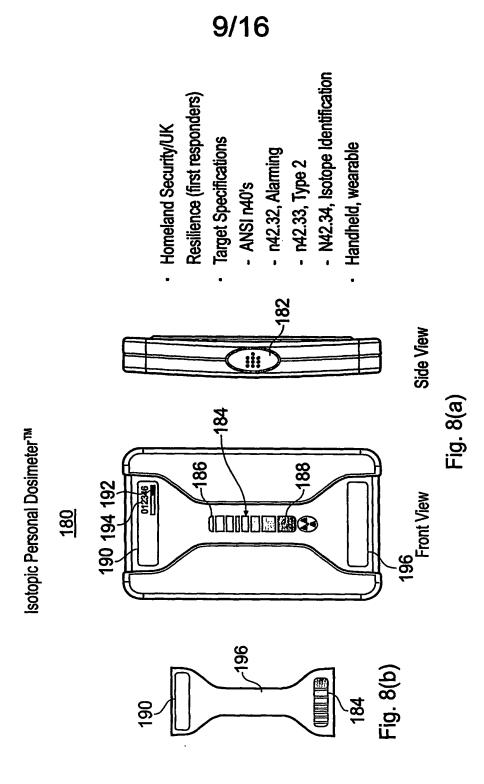
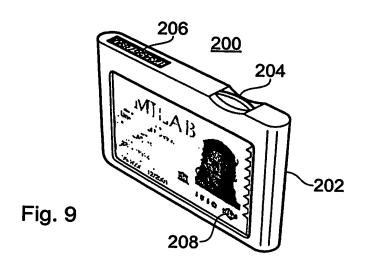
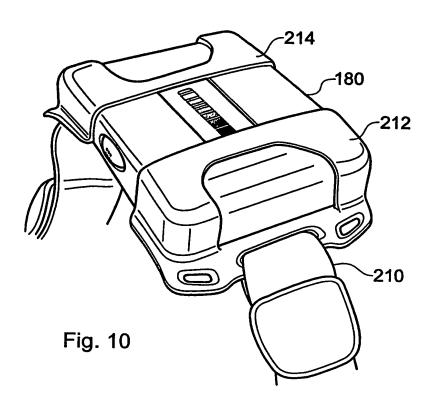
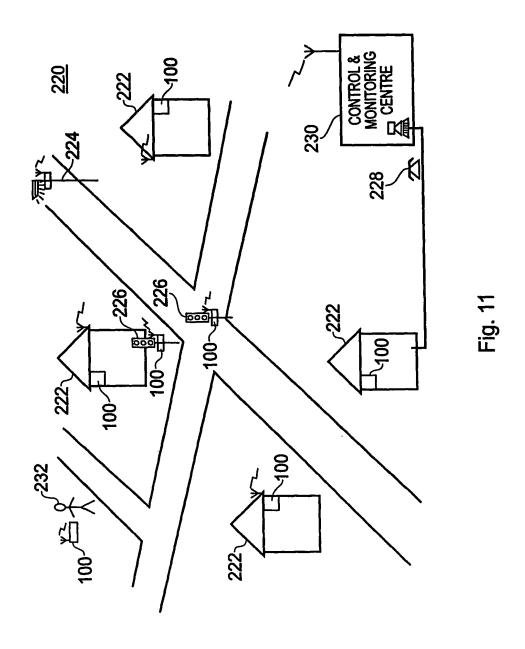


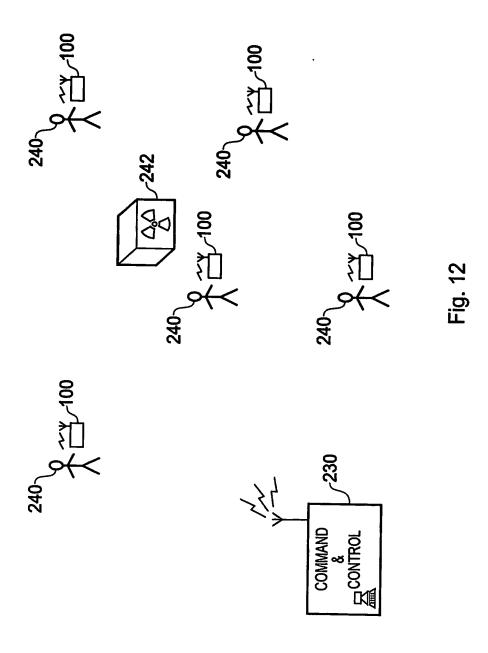
Fig. 7(c)











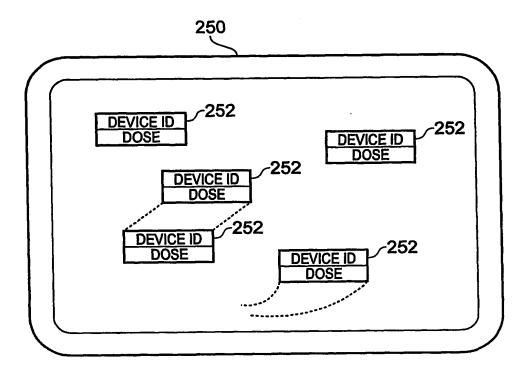


Fig. 13

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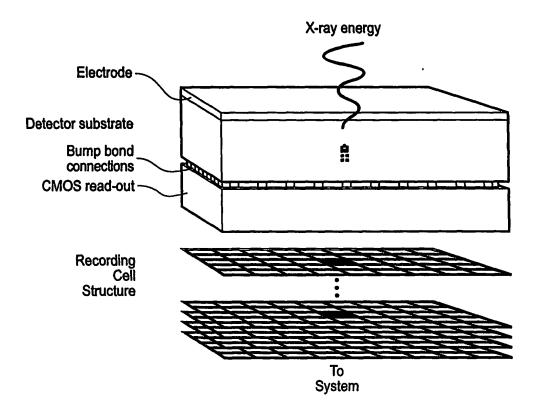


Fig. 14

Drawing exhibits the generic structure of the detector layer, its bonding to the CMOS recording layer and a diagrammatic representation of the cell structure

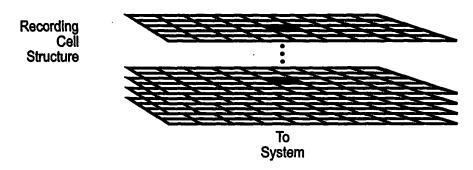


Fig. 15

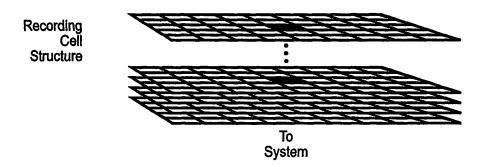


Fig. 16

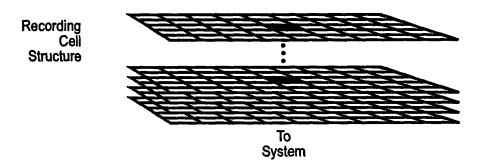


Fig. 17